

MC74VHC1GU04

Unbuffered Inverter

The MC74VHC1GU04 is an advanced high speed CMOS Unbuffered inverter fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation.

This device consists of a single unbuffered inverter. In combination with others, or in the MC74VHCU04 Hex Unbuffered Inverter, these devices are well suited for use as oscillators, pulse shapers, and in many other applications requiring a high-input impedance amplifier. For digital applications, the MC74VHC1G04 or the MC74VHC04 are recommended.

The internal circuit is composed of three stages, including a buffer output which provides high noise immunity and stable output.

The MC74VHC1GU04 input structure provides protection when voltages up to 7V are applied, regardless of the supply voltage. This allows the MC74VHC1GU04 to be used to interface 5V circuits to 3V circuits.

- High Speed: $t_{PD} = 2.5\text{ns}$ (Typ) at $V_{CC} = 5\text{V}$
- Low Power Dissipation: $I_{CC} = 2\mu\text{A}$ (Max) at $T_A = 25^\circ\text{C}$
- Power Down Protection Provided on Inputs
- Balanced Propagation Delays
- Pin and Function Compatible with Other Standard Logic Families
- Latchup Performance Exceeds 300mA

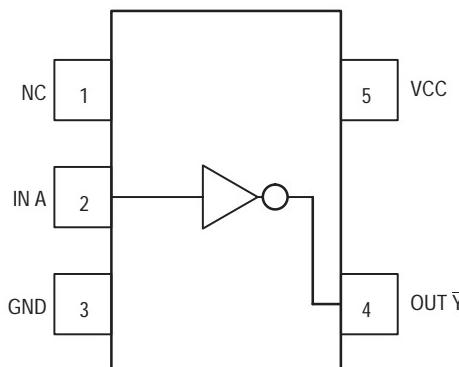
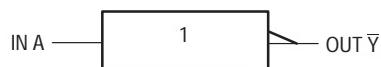


Figure 1. 5-Lead SOT-353 Pinout (Top View)

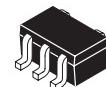
LOGIC SYMBOL



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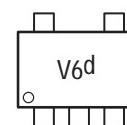
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SC-88A / SOT-353
DF SUFFIX
CASE 419A

MARKING DIAGRAM



Pin 1
d = Date Code

PIN ASSIGNMENT

1	NC
2	IN A
3	GND
4	OUT \bar{Y}
5	VCC

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

FUNCTION TABLE

A Input	\bar{Y} Output
L	H
H	L

MC74VHC1GU04

MAXIMUM RATINGS*

Characteristics	Symbol	Value	Unit
DC Supply Voltage	V _{CC}	-0.5 to +7.0	V
DC Input Voltage	V _{IN}	-0.5 to +7.0	V
DC Output Voltage V _{CC} = 0 High or Low State	V _{OUT}	-0.5 to 7.0 -0.5 to V _{CC} + 0.5	V
Input Diode Current	I _{IK}	-20	mA
Output Diode Current (V _{OUT} < GND; V _{OUT} > V _{CC})	I _{OK}	+20	mA
DC Output Current, per Pin	I _{OUT}	+25	mA
DC Supply Current, V _{CC} and GND	I _{CC}	+50	mA
Power dissipation in still air, SC-88A †	P _D	200	mW
Lead temperature, 1 mm from case for 10 s	T _L	260	°C
Storage temperature	T _{Stg}	-65 to +150	°C

* Maximum Ratings are those values beyond which damage to the device may occur. Exposure to these conditions or conditions beyond those indicated may adversely affect device reliability. Functional operation under absolute—maximum—rated conditions is not implied. Functional operation should be restricted to the Recommended Operating Conditions.

†Derating — SC-88A Package: -5 mW/°C from 65° to 125°C

RECOMMENDED OPERATING CONDITIONS

Characteristics	Symbol	Min	Max	Unit
DC Supply Voltage	V _{CC}	2.0	5.5	V
DC Input Voltage	V _{IN}	0.0	5.5	V
DC Output Voltage	V _{OUT}	0.0	V _{CC}	V
Operating Temperature Range	T _A	-55	+85	°C
Input Rise and Fall Time V _{CC} = 3.3V ± 0.3V V _{CC} = 5.0V ± 0.5V	t _r , t _f	0	No Limit No Limit	ns/V

MC74VHC1GU04

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	V _{CC} (V)	T _A = 25°C			T _A ≤ 85°C		T _A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V _{IH}	Minimum High-Level Input Voltage		2.0 3.0 4.5 5.5	1.7 2.4 3.6 4.4			1.7 2.4 3.6 4.4		1.7 2.4 3.6 4.4		V
V _{IL}	Maximum Low-Level Input Voltage		2.0 3.0 4.5 5.5			0.3 0.6 0.9 1.1		0.3 0.6 0.9 1.1		0.3 0.6 0.9 1.1	V
V _{OH}	Minimum High-Level Output Voltage V _{IN} = V _{IH} or V _{IL}	V _{IN} = V _{IH} or V _{IL} I _{OH} = -50µA	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		1.9 2.9 4.4		V
		V _{IN} = V _{IH} or V _{IL} I _{OH} = -4mA I _{OH} = -8mA	3.0 4.5	2.58 3.94			2.48 3.80		2.34 3.66		V
V _{OL}	Maximum Low-Level Output Voltage V _{IN} = V _{IH} or V _{IL}	V _{IN} = V _{IH} or V _{IL} I _{OL} = 50µA	2.0 3.0 4.5		0.0 0.0 0.0	0.1 0.1 0.1		0.1 0.1 0.1		0.1 0.1 0.1	V
		V _{IN} = V _{IH} or V _{IL} I _{OL} = 4mA I _{OL} = 8mA	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52	V
I _{IN}	Maximum Input Leakage Current	V _{IN} = 5.5V or GND	0 to 5.5			±0.1		±1.0		±1.0	µA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5			2.0		20		40	µA

AC ELECTRICAL CHARACTERISTICS (C_{load} = 50 pF, Input t_r = t_f = 3.0ns)

Symbol	Parameter	Test Conditions	T _A = 25°C			T _A ≤ 85°C		T _A ≤ 125°C		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t _{PLH} , t _{PHL}	Maximum Propagation Delay, Input A to \bar{Y}	V _{CC} = 3.0 ± 0.3V C _L = 15 pF C _L = 50 pF		3.5 4.8	8.9 11.4		10.5 13.0		12.0 15.5	ns
		V _{CC} = 5.0 ± 0.5V C _L = 15 pF C _L = 50 pF		2.5 3.8	5.5 7.0		6.5 8.0		8.0 9.5	
C _{IN}	Maximum Input Capacitance			4	10		10		10	pF

CPD	Power Dissipation Capacitance (Note 1.)	Typical @ 25°C, V _{CC} = 5.0V				pF
		22				

1. CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation: I_{CC(OPR)} = CPD • V_{CC} • f_{in} + I_{CC}. CPD is used to determine the no-load dynamic power consumption; P_D = CPD • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

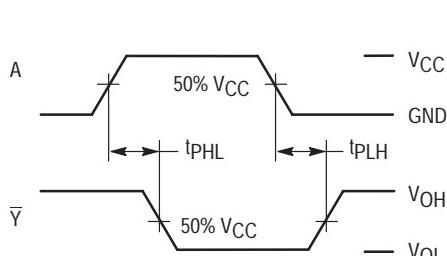
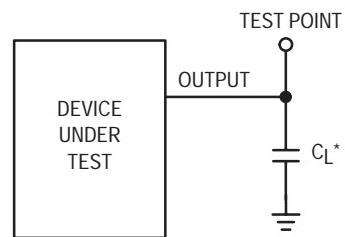


Figure 2. Switching Waveforms



*Includes all probe and jig capacitance

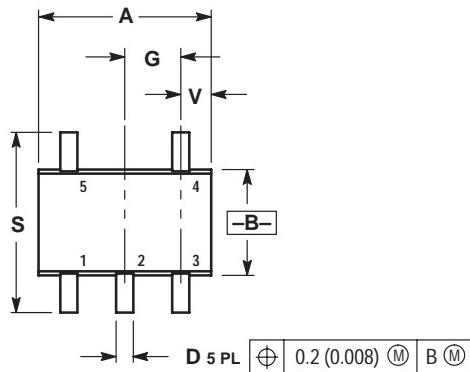
Figure 3. Test Circuit

DEVICE ORDERING INFORMATION

Device Order Number	Device Nomenclature						Package Type	Tape and Reel Size
	Circuit Indicator	Temp Range Identifier	Technology	Device Function	Package Suffix	Tape & Reel Suffix		
MC74VHC1GU04DFT1	MC	74	VHC1G	U04	DF	T1	SC-88A / SOT-353	7-Inch/3000 Unit

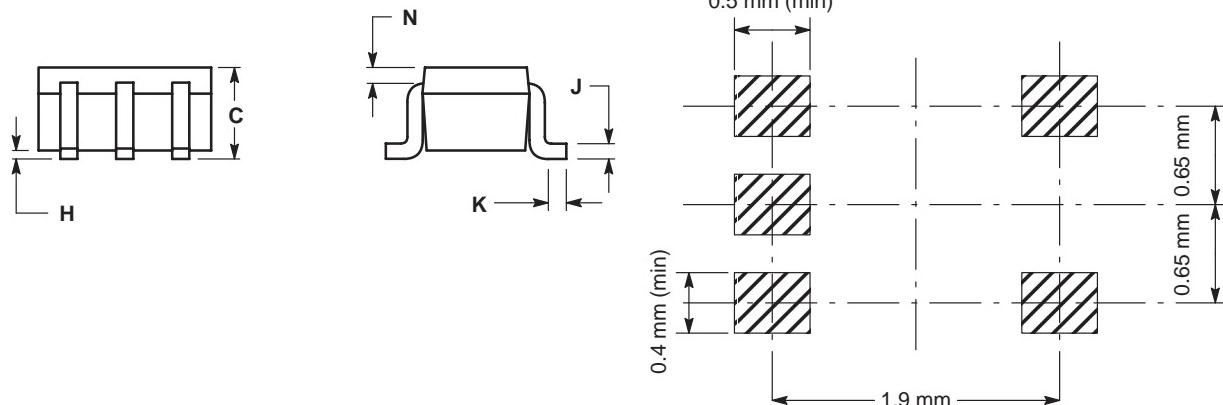
PACKAGE DIMENSIONS

**SC-88A / SOT-353
DF SUFFIX
5-LEAD PACKAGE
CASE 419A-01
ISSUE B**



NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
H	---	0.004	---	0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2.00	2.20
V	0.012	0.016	0.30	0.40



MC74VHC1GU04

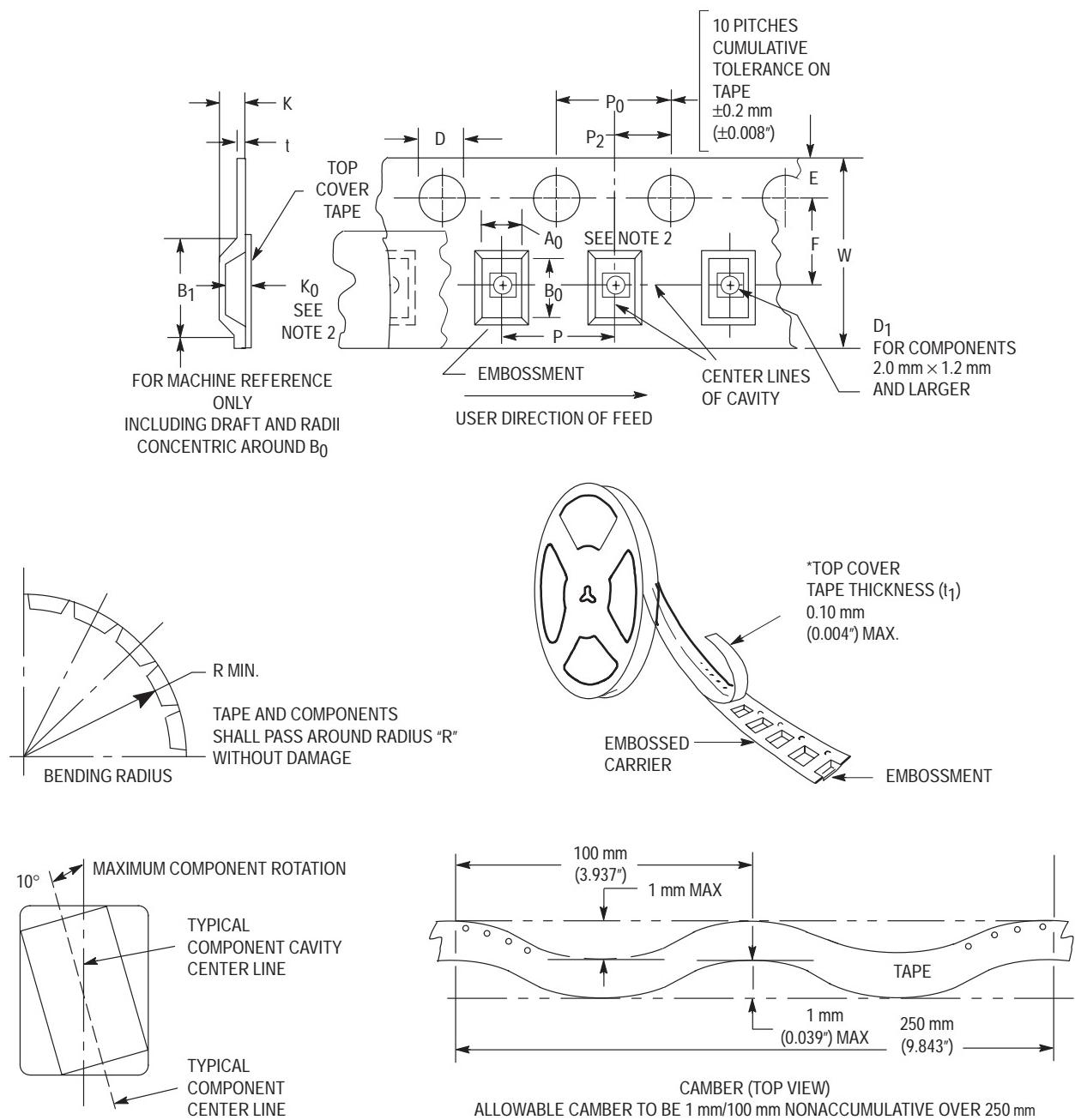


Figure 4. Carrier Tape Specifications

EMBORESSED CARRIER DIMENSIONS (See Notes 1 and 2)

Tape Size	B_1 Max	D	D_1	E	F	K	P	P_0	P_2	R	T	W
8 mm	4.35 mm (0.171")	$1.5 \pm 0.1/-0.0 \text{ mm}$ (0.059 +0.004/-0.0")	1.0 mm Min (0.039")	1.75 $\pm 0.1 \text{ mm}$ (0.069 ±0.004")	3.5 $\pm 0.5 \text{ mm}$ (1.38 ±0.002")	2.4 mm (0.094")	4.0 $\pm 0.10 \text{ mm}$ (0.157 ±0.004")	4.0 $\pm 0.1 \text{ mm}$ (0.156 ±0.004")	2.0 $\pm 0.1 \text{ mm}$ (0.079 ±0.002")	25 mm (0.98")	0.3 $\pm 0.05 \text{ mm}$ (0.01 +0.0038/-0.0002")	8.0 $\pm 0.3 \text{ mm}$ (0.315 ±0.012")

1. Metric Dimensions Govern—English are in parentheses for reference only.
2. A_0 , B_0 , and K_0 are determined by component size. The clearance between the components and the cavity must be within 0.05 mm min to 0.50 mm max. The component cannot rotate more than 10° within the determined cavity

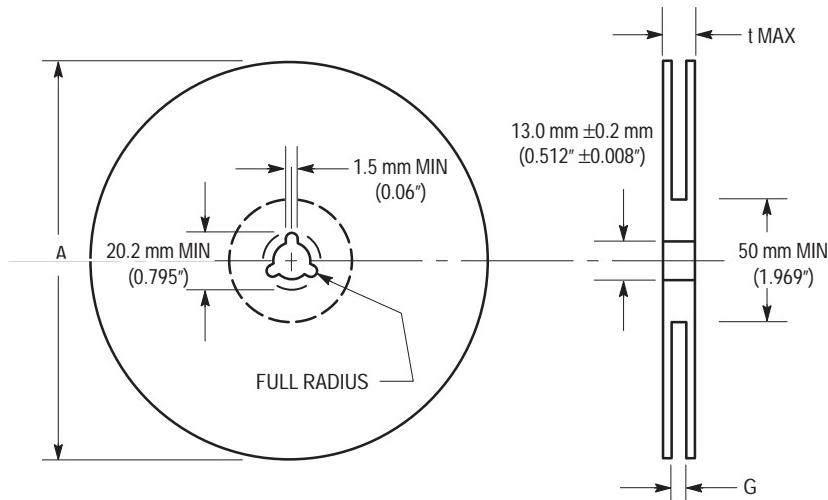


Figure 5. Reel Dimensions

REEL DIMENSIONS

Tape Size	A Max	G	t Max
8 mm	330 mm (13")	8.400 mm, +1.5 mm, -0.0 (0.33", +0.059", -0.00)	14.4 mm (0.56")

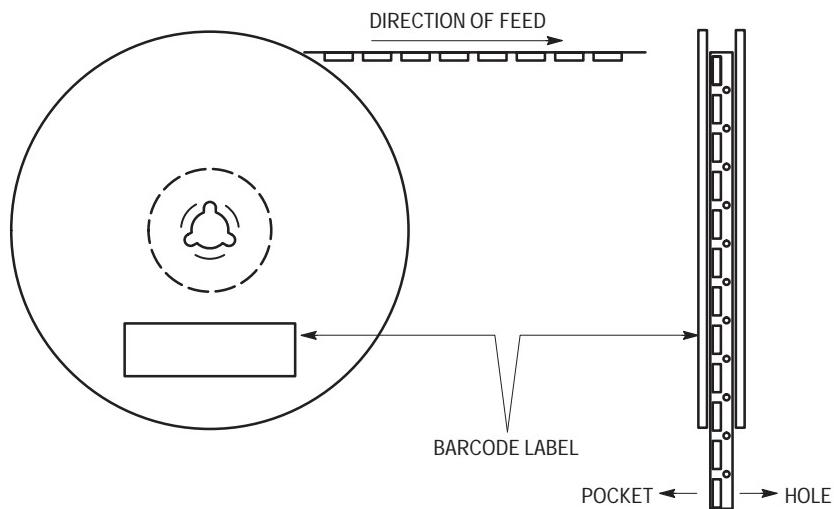


Figure 6. Reel Winding Direction

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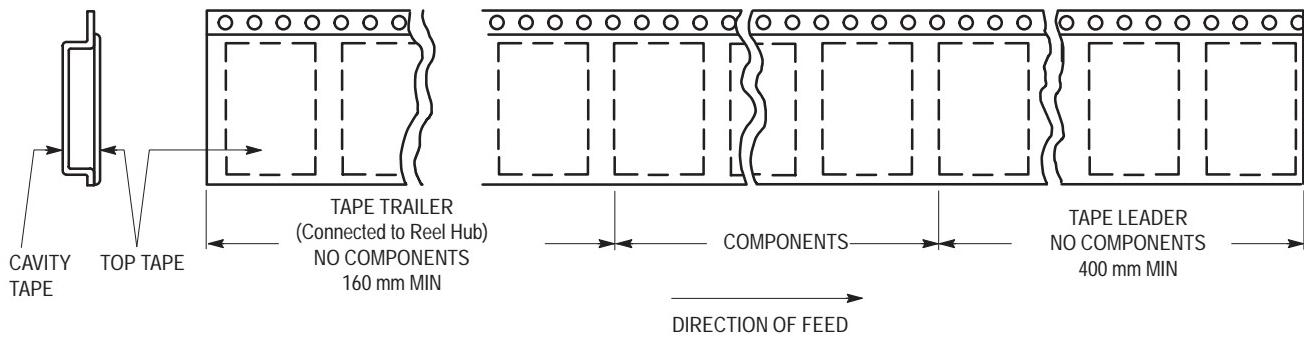


Figure 7. Tape Ends for Finished Goods

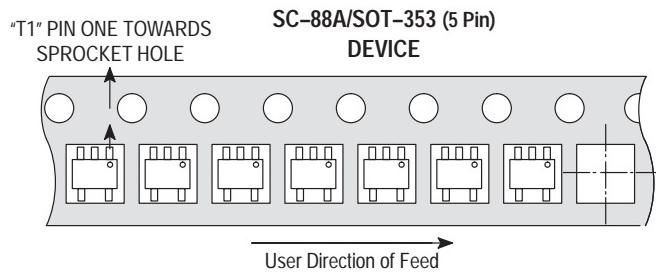


Figure 8. Reel Configuration

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